SEAT No. :

Total No. of Questions : 8] **PB-3647**

[Total No. of Pages : 2

Max. Marks : 70

[6261]-55

S.E. (Information Technology) Processor Architecture

(2019 Pattern) (Semester - IV) (214451)

Time : 2½ Hours] Instructions to the candidates:

- 1) Answer Q.1 or Q.2, Q.3 or Q.4, Q.5 or Q.6, Q.7 or Q.8.
- 2) Neat diagrams must be drawn wherever necessary.
- 3) Figures to the right indicate full marks.
- 4) Assume suitable data, if necessary.

Q1) a) Discuss the steps in executing interrupts in PIC 18 microcontroller. [7]

b) Draw an interfacing diagram for 4×4 matrix keyboard with PICI8F microcontroller and explain it. [8]

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c) Explain function of following LCD pins:

- i) RS ii) RW iii)
- Q2) a) Explain PIR (Peripheral Interrupt Request Register) IPR (Peripheral Interrupt Priority Register).

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- b) Draw and explain the interfacing of relay and buzzer with RIC 18Fxxx microcontroller. [7]
- c) What are peripheral interrupts, IVT and ISR?
- Q3) a) Explain RS232 standard with suitable diagram. [6]
 b) Explain operation of compare mode of PIC18FXX microcontroller with diagram. [6]
 - c) Compare SPI and I2C bus protocols (6¹) [5]
 - *P.T.O.*

[3]

[3]

- OR
- Explain the function CCP1CONSFR along with its format **Q4**) a) **[6]** Distinguish between synchronous and asynchronous serial communication. b) [5] Explain the UART operation in PIC 18FXX with example. c) **[6]** Write steps in programming A to D conversion in PIC 18F microcontroller **Q5**) a) [6] Explain function of any 4 pins of RTC DS1306 b) [8] List out the steps necessary for reading from EEPROM of PIC 18 c) [4] OR **Q6**) a) Draw and explain the interfacing diagram of DAC0808 with PIC 18FXXX. [7] Explain in detail the functions of ADCON0 SFR of PIC 18FXX b) microcontroller. [6] Explain interfacing of LM35 temperature sensor with PIC I8FXX c) microcontroller [5] **Q7**) a) Describe the ARM BUS Technology How ARM instruction set differs from pure RISC definition? b) 15 Explain ARM core dataflow Model with suitable diagram. [6] c) OR Describe the major Design Rules of RISC philosophy. List the features **08**) a) of RISC Processor accepted by ARM processor. [5] Write significance of special registers R13, R14 and R15 in ARM 7 [6] **b**) What are the different operating modes of ARM [6] c) [6261]-55 2