Total No. of Questions : 4]	SEAT No.:	
P1273	[Total No. of Pages : 2	

OCT/FE/INSEM/-6 F.E. (Semester - I)

BASIC ELECTRONICS ENGINEERING (2019 **Pattern**) Time: 1 Hour] [Max. Marks : 30] Instructions to the candidates: Answer Q.1 or Q.2, Q.3 or Q.4 Figure to right indicate full marks. 2) Compare active and passive components explain passive components.[5] **Q1**) a) Explain the operation of full wave Rectifier with suitable diagram and b) wave forms. [5] Explain the construction and working principle of LED. [5] c) OR Explain impact of electronics on Industry. [5] **Q2**) a) Explain the construction and working of = P - N junction diode. Draw its b) V - I characteristics. [5] Draw circuit diagram of zener diode as voltage regulator and Explain it. c) [5] Draw and explain BJT as a switch. **Q3**) a) [5] Explain construction and operation of N - channel EMOSFET. b) [5] For inverting amplifier using op = Amp, if $R_f = 100 \text{ K}\Omega$, $R_1 = 10 \text{K}\Omega$, V_{CC} $= \pm 10V, V_{i} = 2V$ Calcalate output voltage. i) Is the result in part (i) practically possible? Justify. [5]

OR

- Q4) a) Explain construction of BJT with respect to area and doping concentration. Mention the types of BJT.[5]
 - b) Explain construction and operation of p channel EMOSFET. [5]
 - c) Write ideal and practical values of five parameters of op-Amp. [5]

