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## OCT/FE/INSEM/-6

## F.E. (Semester - I) <br> BASIC ELECTRONICS ENGINEERING <br> (2019 Pattern)

## Time: 1 Hour]

[Max. Marks : 30
Instructions to the candidates:

1) Answer Q. 1 or Q.2, Q. 3 or $\mathbf{Q .} 4$
2) Figure to right indicate full marks.

Q1) a) Compare active and passive components explain passive components.[5]
b) Explain the operation of full wave Rectifier with suitable diagram and wave forms.
c) Explain the construction and working principle of LED.

OR
Q2) a) Explain impact of electronics on Industry.
b) Explain the construction and working of $=\mathrm{P}-\mathrm{N}$ junction diode. Draw its V - I characteristics.
c) Draw circuit diagram of zener diode as voltage regulator and Explain it.

Q3) a) Draw and explain BJT as a switch.
b) Explain construction and operation of N - channel EMOSFET.
c) For inverting amplifier using op $=A m p$, if $R_{f}=100 \mathrm{~K} \Omega, \mathrm{R}_{1}=10 \mathrm{~K} \Omega, \mathrm{~V}_{\mathrm{CC}}$ $= \pm 10 \mathrm{~V}, \mathrm{~V}_{\mathrm{i}}=2 \mathrm{~V}$
i) Calcalate output voltage.
ii) Is the result in part (i) practically possible? Justify.

Q4）a）Explain construction of BJT with respect to area and doping concentration．Mention the types of BJT．
b）Explain construction and operation of p －channel EMOSFET．
c）Write ideal and practical values of five parameters of op－Amp．

