Total No.	of Questions : 4] SEAT No. :	
P-5372	[Total No. of Page	s:1
[6185]-55		
F.E. (Insem.)		
BASIC ELECTRONICS ENGINEERING		
(2019 Pattern) (Semester - I) (104010)		
Time : 1 Hour] [Max. Marks :		: 30
Instructions to the candidates:		
1)	Attempt Q.1 or Q.2, Q.3 or Q.4.	
2)	Figures to the right indicate full marks.	
<i>Q1</i> ) a)	Compare half wave rectifier and full wave rectifier.	[5]
b)	Explain the construction and working principle of LED.	[5]
c) \	Explain impact of Electronics on society and industry.	[5]
QR		
<b>Q2</b> ) a)	Compare active and passive components.	[5]
b)	Explain the operation of bidge rectifier circuit with diagram.	[5]
c)	Explain the construction and working of photodiode.	[5]
<b>Q3)</b> a)	Draw an explain input and output characteristics of BJT in com	non
	emitter configuration, show different regions of operation.	[5]
b)	Compare BJT and MOSFET.	[5]
c)	Draw and explain Internal Block diagram of OP AMP.	[5]
	OR OF OV	
<b>Q4</b> ) a)	Explain with suitable diagram operation of NPN transistor	[5]
b)	Explain construction and operation of n-channel E-MOSFET.	[5]
<b>c</b> )	State and explain practical parameters of OP-AMP.	[5]
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