Total No. of Questions : 4]

SEAT No. :

PC-231

[Total No. of Page : 1

[6361]-97

B.E. (E & Tc) (Insem.)

ELECTRONIC PRODUCT DEVELOPMENT

(2019 Pattern) (Semester - VII) (Elective - IV) (404185B)

Time : 1 Hour] *[Max. Marks : 30*

Instructions to the candidates:

1) ***Answers Q.1 or Q.2, Q.3 or Q.4.***
2) ***Neat diagrams must be drawn wherever necessary.***
3) ***Figures to the right side indicate full marks.***
4) ***Your answers will be valued as a whole.***
5) ***Use of logarithmic tables slide rule, Mollier charts, electronic pocket calculator and steam tables is allowed.***
6) ***Assume Suitable data, if necessary.***

Q1) a) Explain six stages of concept development. **[5]**

b) Explain Integration. Validation and verification (IVV) in detail. **[5]**

c) Explain the steps involved in completion of Life Cycle Cost Anallysis (LCCA). **[5]**

OR

Q2) a) Explain types of Rapid Prototyping. **[5]**

b) Explain design concerns and Heuristics. **[5]**

c) Explain common reasons of product failure. **[5]**

Q3) a) Explain methods to investigate circuit operation and design. **[5]**

b) Explain in brief types of output interface in electronic product. **[5]**

c) Explain the factors affecting high speed design. **[5]**

OR

Q4) a) Explain different types of internal noise in electronic product. **[5]**

b) Explain fault tolerance in terms of careful design, tastable functions and redundent architecture. **[5]**

c) Explain in brief different types of Input interfaces used in electronic product? **[5]**