Total N	No. of Questions : 4]	0		SEAT No. :		
PE52		9]-354	,	[Total	No. of Page	es:1
T.E. (E& TC/Electronics Engg. (VLSI Design and Technology))						
COMPUTERNETWORKS						
(2019 Pattern) (Semester - I) (304185 (D)) (Elective - I) (Insem)						
	9, 5			-		
	1 Hour]			[N	Iax. Marks	: 30
Instructions to the candidates: 1) Answer any Two questions.						
2)		ever ned	cessarv.)	
3)						
4)						
5)	Assume suitable data, if necessary.			1		
	, , , , , , , , , , , , , , , , , , ,			N		
01) -)	E	D: 1-		CT X-1 A	- NI - 41	- [0]
Q1) a)) Enumerate the Advantages and	Disady	antages c	of Local Are	a Networi	[6].2
b)) Draw the TCP/IP reference mod	del and	write the	functions of	f each laye	r.[7]
ĺ			4 3.)	•	
	C)R				
Q2) a)) Enumerate the Advantages and	Disady	antages o	f OSI refere	ence Mode	1 [7]
Q_{2}						1.[/]
b)) Explain with diagram any four	functi	ons of the	Data Link	Layer.	[8]
(12) (2)	Evaluin control field of WDI C	Tayo t I	Eromo	Eromo on	d II Erom	. [7]
Q3) a)	Explain control field of HDLC	w.e.t i	-Frame, S	S-Frame and	ı U-Flaille	5.[/]
b)) A data bit word 1011 is to be	e transi	mitted. C	onstruct the	e Even Pa	arity
ĺ	7-Bit Hamming Code.					[8]
					×	3
	0.60.					
	C)R				
Q4) a)	Explain the Stop and Wait Pro	ntocol i	n Noisele	ess Channel	with deta	ailed
Q4) a)	schematic.	nocoi i	II INOISCIC		with deta	
	schematic.			77 6)	[7]
b)) Explain in details, working of I	PPP wi	th State T	ransition D	iagram.	[8]
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			70	20		
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V	- H -	M	(·)	-		
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