		o. of Questions : 8]  SEAT No.:	D 2			
P29	<b>)</b> 55	[Total No. of 1]	Pages: 2			
T.E. (£ & TC)						
MICROCONTROLLERS						
(2015) Pattern) (Semester - I)						
Time	$2:2^{1/2}$	<sup>1</sup> / <sub>2</sub> Hours] [Max. Ma	[Max. Marks : 70			
Instr	uctio	tions to the candidates:				
	<i>1</i> )	Answer Q.1 or Q.2, Q.3 or Q.4, Q.5 or Q.6 and Q.7 or Q.8.				
	<i>2</i> )	Neat diagrams must be drawn wherever necessary.	•			
	<i>3</i> )	Figures to the right side indicate full marks.				
	4)	Assume Suitable data if necessary.				
	<i>5</i> )	Use of Calculator is allowed.				
0.71			F 63			
Q1)	a)	Explain IE and IP register of 8051 with their priorities.	[6]			
	b) 1	Interface 8 bit LCD with 8051 to display SPPU on first line and P	UNE on			
		second line.	[8]			
	c)	Explain Memory organization of 8051.	[6]			
		OR				
Q2)	a)	Explain instruction ADD, ANL and DJNZ.	[6]			
	b)	Explain interface of DAC with 8051. Write a program to g	generate			
	ŕ	triangular wave.	[8]			
	c)	Write short note on IDE and Logic Analyser.	<b>5</b> [6]			
			9			
()2)	۵) (ه	Evaloin the detailment anomination of DIC some Order Dick	." .1- a.1.a.4			
Q3)	a)	Explain the data memory organization of PIC, comment on ban				
		register and access banks.	[8]			
	b)	State different power management modes and explain idle and sleep r	node.[ <b>8</b> ]			
		OR				
<b>Q4</b> )	a)	Explain RESET operation of PIC18F458 in detail.	[8]			
•	b)	What are the various oscillator options? How can it be selected	ed using			
	,	Config register.	[8]			

Q5)	a)	Explain the concept of PWM used in PIC 18F458 controlle	r with
		example.	[9]
	b)	Explain different ports of PIC 18Exx microcontroller along with S	FR.[ <b>9</b> ]
		OR	
<b>Q6</b> )	a)	Interface 4X4 Keypad to PIC 18Fxx controller and explain.	Write
		embedded C program to display key press?	[9]
	b)	State the programming steps for generation of time delay using Time	mer.[ <b>9</b> ]
			${\sf G}^{\sim}$
<b>Q7</b> )	a)	Draw and explain MSSP structure of PIC18F458.	[8]
	b)	Explain Rs-232 and RS-485 Protocol in details.	[8]
		OR	
<b>Q8</b> )	a)	Explain I <sub>2</sub> C Protocol in details and compare I <sub>2</sub> C RS-232 and F	RS-485
		protocols.	[8]
	b) (	Explain the step wise procedure and design methodology of P.	IC test
	(	board.	[8]
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