

Total No. of Questions : 4]

SEAT No. :

PC398

[6359]-518

[Total No. of Pages : 1

S.E. (Electronics & Computer Engineering) (Insem)

COMPUTER ORGANIZATION

(2019 Pattern) (Semester-III) (204203)

Time : 1 Hour]

[Max. Marks : 30

Instructions to the candidates:

- 1) Answer Q1 or Q2, Q3 or Q4.
- 2) Figures to the right indicates full marks.
- 3) Assume suitable data if necessary.

**Q1)** a) Draw the block diagram of basic structure of computer. Explain each block in detail. [8]

b) Give the IEEE standard for floating point numbers for: [7]

- i) Single precision number
- ii) Double precision number

OR

**Q2)** a) Explain single bus and multi bus structure. [8]

b) Explain Booths algorithm with suitable example. [7]

**Q3)** a) Explain memory hierarchy of computer system. [8]

b) Explain cache mapping techniques. [7]

OR

**Q4)** a) Explain virtual memory in detail. [8]

b) Write a note on semiconductor RAM memory. [7]

