Total	l No. o	of Questions : 6] SEAT No. :	
P26	3 2	[Total No. of Pages : 1	
1 20		BE/INSEM/APR-590	
B.E. (Computer Engineering)			
410252 (C): EMBEDDED AND REAL TIME OPERATING SYSTEM			
(2015 Pattern) (Semester - II) (Elective - III)			
Time: 1 Hour] [Max. Marks: 30			
Instructions to the candidates:			
	<i>1</i>)	Neat diagrams must be drawn wherever necessary.	
	2)	Figures to the right indicate full marks.	
	<i>3</i>)	Assume suitable data, if necessary.	
Q 1)	a)	What is embedded system? List and explain different challenges of embedded system. [5]	
	b)	With neat diagram, explain the components of Embedded system hardware. [5]	
	6	OR OR	
Q2)	a)	What are different types of processor technologies used in embedded	
~	ĺ	system design. [5]	
	b)	List and Explain software tools used for designing of an embedded	
		system. [5]	
02)	`		
Q3)	a)	What is the use of I2C bus? Draw and explain the I2C data frame format.	
	b)	Describe different forms of memories in the embedded system.	
	U)	OR	
<i>Q4</i>)	a)	Explain in detail DSP Processor Architecture with core architectural block	
~ .		diagram. [5]	
	b)	Enlist various Internet enabled embedded system protocols and explain	
		any one with its features and diagram. [5]	
05)		What is HDLC2 Faulain different from Aug 1 2 Aug 6	
<i>Q</i> 5)	a)	What is HDLC? Explain different frame types and data transfer modes of HDLC. [5]	
	b)	of HDLC. [5] Explain why PCI/X buses are used for high speed data transfer? List the	
	0)	major features of PCI/X bus. [5]	
		OR OR	
Q6)	a)	Explain Serial protocols RS-232C & RS-485. [5]	
	b)	Explain in detail Parallel ports I/O Interfacing with neat diagram. [5]	