Total No. of Questions: 8]			estions: 8] SEAT No. :				
PD4083			[Total N	To. of Pages : 2			
			[6402]-43				
S.E. (Computer Engineering)							
MICROPROCESSOR							
(2019 Pattern) (Semester - IV) (210254)							
Time	· 21	⁄2 Hour	rs I	lax. Marks : 70			
Instructions to the candidates:							
	1)		er Q.1 or Q.2, Q.3 or Q.4, Q.5 or Q.6, Q.7 or Q.8.				
	2) 3)		liagram must be drawn wherever necessary.  es to the right indicate full marks.	<b>つ</b>			
	1) 1)	_	ne suitable data, if necessary.	76			
	,						
Q1)	a)	Fyr	Plain following in detail.	[6]			
<b>Q1</b> )	u)	- 20-	Global Descriptor Table	[0]			
		ii)	Local Descriptor Table				
	b)	Dra	w and explain the general descriptor format.	[6]			
	c)		plain the process of Segment translation in detail.	[6]			
		_					
			OR	C			
<i>Q2</i> )	a)	Exp	olain following in detail	.[6]			
		i)	GDTR				
		ii)	LDTR &	2			
		iii)	IDTR 🔊	· .			
	b)	Dra	w and explain segment selector format.	[6] [6]			
	c)	Exp	plain the process of page translation in detail.	[6]			
<i>Q3</i> )	a)	Exp	lore the need for a protection mechanism in 80386.	[6]			
	b)	List	and explain various Privilege Instructions.	[6]			
	c)	Exp	lore five aspects of protection applied in segmentation.	[5]			
			OR 96.				
				DTA			
			No.	P.T.O.			

<i>Q4</i> )	a)	Explain different levels of protection? State the rules of protection ch	eck. [6]
	b)	Write a short note on CPL, DPL, and RPL.	[6]
	c)	What is call gate? Explain how it is used in calling functions with his privilege levels.	gher [5]
<b>Q</b> 5)	a)	Draw and Explain the Task State Segment of 80386.	[6]
	b)	Explore the role of Task Register in multitasking and the instruct used to modify and read Task Register.	ions [ <b>6</b> ]
	c)	List and explain various features of virtual 8086 Mode.  OR	[6]
<b>Q6</b> )	a)	Explain the TSS descriptor of 80386 with a neat diagram.	[6]
	b)	Define task switching and explain the step involved in task switch operation.	hing [ <b>6</b> ]
	c)	Explore memory management in the virtual 8086 Mode.	[6]
<b>Q</b> 7)	a)	Explain the process of Enabling and Disabling Interrupts is 80386.	[6]
	b)	Explain different types of exceptions in 80386 with suitable examples	s. [ <b>6</b> ]
	c)	Differentiate between Microprocessor and Microcontroller.	[5]
		OR	
<b>Q</b> 8)	a)	How are interrupts identified in 80386?	[6]
	b)	How are interrupts identified in 80386?  Explain the following exceptions in brief  i) Overflow,  ii) Divide error  iii) invalid opcode	[6]
		ii) Divide error	
		iii) invalid opcode	
	c)	With the help of neat diagram explain the architecture of typ microcontroller.	ical [5]