

Total No. of Questions : 8]

SEAT No. :

P2297

[Total No. of Pages : 2

[5869]-278

S.E. (Computer Engineering)
SOFTWARE ENGINEERING (210253)
(2019 Pattern) (Semester - IV)

Time : 2½ Hours]

[Max. Marks : 70

Instructions to the candidates:

- 1) *Solve Q.1 or Q.2, Q.3 or Q.4, Q.5 or Q.6, Q.7 or Q.8.*
- 2) *Neat diagrams must be drawn wherever necessary.*
- 3) *Assume suitable data, if necessary.*

Q1) a) Design and discuss the project decomposition and work task communication process. [8]

b) Discuss any 2 of the following with suitable example: [10]

- i) FP-Based Estimation
- ii) Object Point (OP)-based estimation.
- iii) Process-Based Estimation.

OR

Q2) a) Describe any two software size estimation techniques. [8]

b) Discuss any 2 of the following with suitable example: [10]

- i) Problem-Based Estimation
- ii) LOC-Based Estimation
- iii) Project Scheduling and basic principles of project scheduling.

Q3) a) List the design concepts. Explain refinement and refactoring. Give the importance of Refactoring in improving the quality of software. [9]

b) List the different architectural styles. Explain any two in detail. [8]

OR

Q4) a) Enlist and explain Component level design steps in detail. [9]

b) Differentiate between followings. [8]

- i) Cohesion and coupling in context of software design? How are these useful for good design of a system?
- ii) Abstraction and Refinement.

P.T.O.

- Q5)** a) Explain Risk identification process? What are the different categories of risks? [8]
b) Write Short Note: [10]
i) Layers of SCM Process
ii) RMMM Plan

OR

- Q6)** a) Explain Risk Projection and Risk Refinement in detail. [8]
b) Explain the change control mechanism in SCM. [10]

- Q7)** a) Explain STLC (Software Testing Life Cycle). [7]
b) Explain the following: [10]
i) Unit testing and integration testing.
ii) White box testing and black box testing.

OR

- Q8)** a) Explain phases in Verification and Validation model with suitable diagram. [7]
b) Discuss any 2 of the following in detail. [10]
i) Acceptance Testing
ii) Tools for Automated Testing and feature.
iii) Defect Life Cycle.

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